

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : Unassigned  
Applicant : Kumar Ganapathy, et al.  
Filed : 09/19/2003  
Title : INSTRUCTION SET ARCHITECTURE FOR SIGNAL  
PROCESSORS (As Amended)

Docket No. : 42P14031D2D  
Customer No. : 8791

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Which is a Divisional of:

Appl. No. : 10/215,721  
Applicant : Ganapathy, et al.  
Filed : 09/19/2000  
Title : INSTRUCTION SET ARCHITECTURE FOR SIGNAL  
PROCESSORS (As Amended)  
TC/A.U. : 2181  
Examiner : Kenneth S. Kim

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Which is a Divisional of:

Appl. No. : 09/494,608  
Applicant : Ganapathy, et al.  
Filed : 01/31/2000  
Title : DYADIC OPERATIONS INSTRUCTION PROCESSOR WITH  
CONFIGURABLE FUNCTIONAL BLOCKS (As Amended)

PRELIMINARY AMENDMENT ACCOMPANYING  
35 USC 120 and 37 CFR 1.53(b)(1) DIVISIONAL APPLICATION

Mail Stop New Application  
Commissioner for Patents  
Alexandria, VA 22313

Dear Sir:

Prior to a first examination in the 35 USC 120 and 37 CFR 1.53(b)(1) divisional application filed herewith, please enter the following amendments:

**Title Amendments** begin on page 2.

**Specification Amendments** begin on page 3.

**Claim Amendments** begin on page 4.

**Remarks** begin on page 10.

**Conclusion with signature** is on page 13.